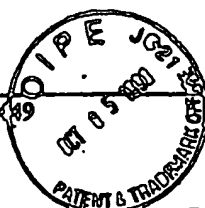


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## INFORMATION DISCLOSURE STATEMENT

(Use several sheets if necessary)

APPLICANT

ELIYAHOU HARARI et al.

FILED

June 23, 1998

GROUP

2785-2827

## U. S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE
W	A12 4 4 7 9 2 1 4	10/23/84	Ryan			
W	A13 4 4 9 3 0 7 5	1/8/85	Anderson et al.			
W	A14 4 4 9 8 1 4 6	2/5/85	Martinez			
W	A15 4 5 2 5 8 3 9	6/25/85	Nozawa et al.			
W	A16 4 5 1 4 8 3 0	4/30/85	Hagiwara et al.			
W	A17 4 5 2 7 2 5 1	7/2/85	Nibby Jr. et al.			
W	A18 4 5 3 0 0 5 4	7/16/85	Hamstra et al.			
W	A19 4 5 6 3 7 5 2	1/7/86	Pelgrom et al.			
W	A20 4 6 0 1 0 3 1	7/15/86	Walker et al.			
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## FOREIGN PATENT DOCUMENTS

DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS	TRANS.? (YES/NO)
B6 EP 0 3 9 2 8 9 5	—1990—	EPO 1990.10.17			
B7 EP 0 2 4 3 5 0 3	—1990—	EPO 1987.11.04			
B8 EP 0 4 9 2 1 0 6	—1992—	EPO 1992.07.01			
B9 EP 0 5 2 2 7 8 0	—1992—	EPO 1993.01.13			
B10 EP 0 5 6 9 0 4 0	—1993—	EPO 1993.11.10			

## OTHER DOCUMENTS (Including Author, Title, Date, Pertinent pages, Etc.)

C4	Williams, "Asymmetric Memory Hierarchies", Communications of the Association for Computing Machinery, vol. 16, no. 4, April 1973, pp. 213-222.

EXAMINER

V. Nguyen

DATE CONSIDERED

6/10/99

\* EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

mg  
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